

Typical Expected Performance- Thermal Resistance

Thermal resistance is a measure of the ability of heat to flow from the PCB to the heat sink. The retainer applies pressure between the board and the heatsink for maximum heat flow or low resistance. A schematic diagram of the test apparatus is shown in figure 7. It was determined experimentally that approximately 70 percent of the heat flows directly from the card to the heat sink and the remaining 30 percent flows through the card retainer to the heat sink. The thermal resistance is by conduction only and no thermal grease was used. The PCB card was .063" thick 6061 Aluminum. Thermal resistance values are for one retainer as shown in figure 8.

Sample thermal calculation:

A pair of 6" 40-5 series wedge-loks are used to secure a circuit board that dissipates 60W of power. Estimate the temperature difference between the board and the heat sink assuming conduction only. The dissipative components are located on the edge of the board closest to the heat sink.

From figure 8. $R = 1.66 \frac{^{\circ}\text{Cin}}{\text{W}}$ for a 40-5-12-LF Wedge-Lok.

$$R_6 = 1.66 \frac{^{\circ}\text{Cin}}{\text{W}} \times \frac{1}{6\text{in}} = 0.28 \frac{^{\circ}\text{C}}{\text{W}}$$

$$\Delta T = Q R_6$$

Where: ΔT is the temperature difference in centigrade

R is the thermal resistance per unit length

R_6 is the thermal resistance for a six inch Wedge-Lok

Lok

Q is the power dissipation in Watts

$$\Delta T = \frac{60\text{W}}{2} \times 0.28 \frac{^{\circ}\text{C}}{\text{W}} = 8.4 \text{ }^{\circ}\text{C}$$

Note: The power dissipated is divided by two because there are a pair of Wedge-Loks.

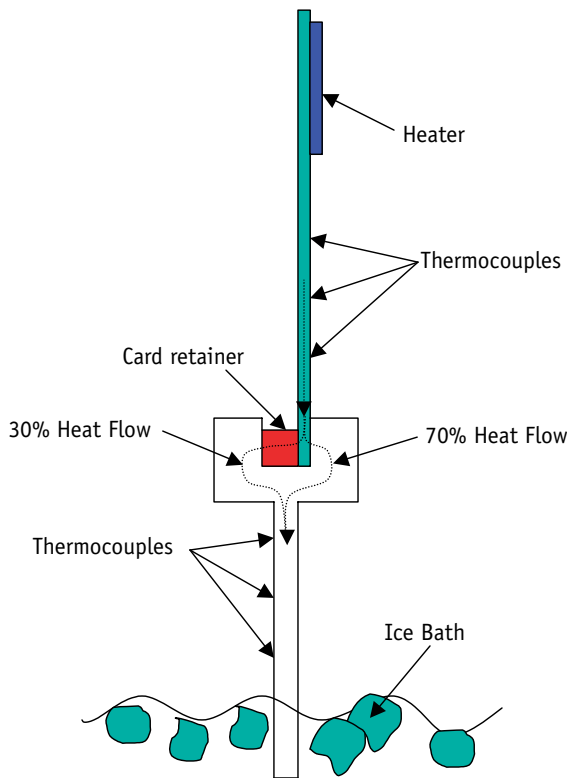


Figure 7. Thermal Test Apparatus Schematic Diagram

Figure 8. Thermal Resistance

